

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

# 7/Election  
7-3-02  
R. Shuler

In re U.S. Patent Application of

SAITO *et al.*

Application Number: 09/881,716

Filed: June 18, 2001

For: SEMICONDUCTOR DEVICE AND PROCESS  
FOR PRODUCTION THEREOF

Attorney Docket No. HITA.0060

Honorable Assistant Commissioner  
for Patents  
Washington, D.C. 20231



Art Unit 2811

Examiner Costanzo, P.

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TECHNOLOGY CENTER 2800

**RESPONSE AND AMENDMENT UNDER 37 C.F.R. § 1.111**

Sir:

This is in response to the Office Action dated June 3, 2002, the shortened period for response to which will expire on July 3, 2002. Applicants hereby elect the continuing prosecution of Group I, Claims 1-12 and 18-20, drawn to a semiconductor device, classified in class 257, subclass 787, without traverse.

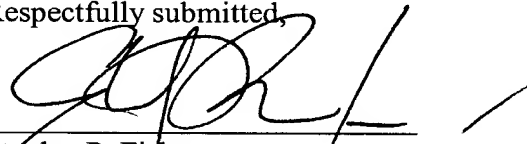
**REMARKS**

The above election along with the following remarks are being submitted as a full and complete response to the Official Action dated June 3, 2002, the period for response to which will expire on July 3, 2002. The Examiner is respectfully requested to review the substance of Claims 1-12, and 18-20 and to indicate the allowability of the claims.

Claims 1-12 and 18-20, drawn to a semiconductor device, classified in class 257, subclass 787, have been elected in this application, while the other claims 13-17 are withdrawn from further prosecution without prejudice. Applicants hereby reserve the right to file a divisional application on any or all of the non-elected claims.

Substantive consideration of the elected claims is respectfully solicited. Should there be any outstanding issues requiring discussion that would further the prosecution and allowance of the above-captioned application, the Examiner is invited to contact the Applicants' undersigned representative at the address and phone number indicated below.

Respectfully submitted,

  
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**June 28, 2002**

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